

MAY 14 2007

Appln. No.: 10/762,156
Amend/Response After Final, filed May 14, 2007
Replying to Office Action of Feb. 14, 2007

PATENT
MS: AFTER FINAL
A.U. 2811 - Expedited Processing Requested
Attorney Docket No. 352003-991320

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/762,156 Conf. No.: 4963
First Named Inventor : Naohito Kojima
Filing Date : January 20, 2004
Art Unit : 2811
Examiner : NADAV, Ori

Atty. Docket No. : 352003-991320
Customer No. : 26379

Title: **SEMICONDUCTOR INTEGRATED CIRCUIT HAVING AN OBLIQUE GLOBAL
SIGNAL WIRING AND SEMICONDUCTOR INTEGRATED CIRCUIT WIRING
METHOD**

RESPONSE TO FINAL OFFICE ACTION /
REQUEST FOR RECONSIDERATION

Mail Stop: AFTER FINAL
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In reply to the final Office Action of February 14, 2007, Applicants respectfully propose that the Office consider the following remarks:

Remarks begin on page 2 of this paper.

do not enter

O.V. 5/24/07

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